

ALLNAMES:(ITW GSE ApS)

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1. [2778058](#) RECTIFICADOR DE 12 PULSOS CON CONDENSADORES DE ENTRADA PARA LA MEJORA DEL FACTOR DE POTENCIA ES - 07.08.2020

Int.Class [H02M 7/08](#) Appl.No 13176268 Applicant ITW GSE ApS Inventor Dahl, Søren Risgaard

Circuito rectificador dispuesto para rectificar energía eléctrica, que comprende - una entrada [202] de alimentación trifásica, - un circuito [210] divisor magnético que está dispuesto para recibir la entrada [202] de alimentación trifásica y dividir la alimentación trifásica en un primer sistema [212] trifásico y un segundo sistema [214] trifásico, teniendo dicho primer sistema trifásico señales retrasadas con relación a las señales del segundo sistema trifásico, - un rectificador [220] de doce pulsos con seis terminales de entrada para conectar los sistemas trifásicos primero y segundo, y para generar una alimentación eléctrica rectificadora a una salida de alimentación, caracterizado por que el circuito rectificador comprende, además - una inductancia [205] trifásica conectada en serie con la entrada de alimentación trifásica y el circuito divisor magnético, y en el que la inductancia [205] trifásica está dispuesta para asegurar el modo de conducción continua en el rectificador [220] de doce pulsos, - múltiples condensadores [230] de corrección de factor de potencia (PFC), que comprenden cada uno terminales primero y segundo, estando dichos primeros terminales conectados a terminales de entrada respectivos del rectificador de doce pulsos, y estando dichos segundos terminales conectados a al menos un punto [236] eléctrico común, - en el que los múltiples condensadores [230] de PFC comprenden seis condensadores.

2. [20160149510](#) RECTIFIER WITH IMPROVED POWER FACTOR US - 26.05.2016

Int.Class [H02M 7/08](#) Appl.No 14904448 Applicant ITW GSE APS Inventor Søren Risgaard Dahl

A rectifier circuit being arranged for rectifying electrical power, comprising a three phase power input, a magnetic splitter circuit being arranged for receiving the three phase power input and splitting the three phase power into a first three phase system and a second three phase system, the first three phase system having signals lagging signals of the second three phase system, a twelve pulse rectifier with six input terminal to connect the first and the second three phase system, and to generate a rectified electrical power at a power output, a three phase inductance being connected in series with the three phase power input and the magnetic splitter circuit, and a plurality of power factor correction (PFC) capacitors, each comprising first and second terminals, said first terminals being connected to respective input terminals of the twelve pulse rectifier, and the second terminals being connected to at least one common electrical point.

3. [WO/2015/004622](#) 12-PULSE RECTIFIER WITH INPUT CAPACITORS FOR POWER FACTOR IMPROVEMENT WO - 15.01.2015

Int.Class [H02M 7/08](#) Appl.No PCT/IB2014/062990 Applicant ITW GSE APS Inventor DAHL, Søren Risgaard

A rectifier circuit being arranged for rectifying electrical power, comprising a three phase power input, a magnetic splitter circuit being arranged for receiving the three phase power input and splitting the three phase power into a first three phase system and a second three phase system, the first three phase system having signals lagging signals of the second three phase system, a twelve pulse rectifier with six input terminal to connect the first and the second three phase system, and to generate a rectified electrical power at a power output, a three phase inductance being connected in series with the three phase power input and the magnetic splitter circuit, and a plurality of power factor correction (PFC) capacitors, each comprising first and second terminals, said first terminals being connected to respective input terminals of the twelve pulse rectifier, and the second terminals being connected to at least one common electrical point.

4. [2824819](#) 12-PULSE RECTIFIER WITH INPUT CAPACITORS FOR POWER FACTOR IMPROVEMENT EP - 14.01.2015

Int.Class [H02M 7/08](#) Appl.No 13176268 Applicant ITW GSE APS Inventor DAHL SØREN RISGAARD

A rectifier circuit being arranged for rectifying electrical power, comprising a three phase power input [202], a magnetic splitter circuit [210] being arranged for receiving the three phase power input and splitting the three phase power into a first three phase system and a second three phase system, the first three phase system having signals lagging signals of the second three phase system, a twelve pulse rectifier [220] with six input terminal to connect the first and the second three phase system, and to generate a rectified electrical power at a power output [224,225], a three phase inductance [205] being connected in series with the three phase power input [202] and the magnetic splitter circuit [210], and a plurality of power factor correction (PFC) capacitors [230], each comprising first [235] and second terminals [236], said first terminals [235] being connected to respective input terminals [212a, 212b, 212c, 214a, 214b, 214c] of the twelve pulse rectifier [220], and the second terminals [236] being connected to at least one common electrical point.

