



- 24 [10,901,244](#) [Method and system for a low parasitic silicon high-speed phase modulator using PN finger waveguides](#)
- 25 [10,892,845](#) [Method and system for a free space CWDM MUX/DEMUX for integration with a grating coupler based silicon photonics platform](#)
- 26 [10,879,648](#) [Method and system for large silicon photonic interposers by stitching](#)
- 27 [10,873,399](#) [Method and system for a photonic interposer](#)
- 28 [10,866,482](#) [Method and system for a vertical junction high-speed phase modulator](#)
- 29 [10,855,378](#) [Method and system for a silicon-based optical phase modulator with high modal overlap](#)
- 30 [10,855,229](#) [Method and system for process and temperature compensation in a transimpedance amplifier using a dual replica](#)
- 31 [10,848,246](#) [Method and system for an optical connection service interface](#)
- 32 [10,833,773](#) [Method and system for waveguide delay based equalization with optical splitting in optical communication](#)
- 33 [10,819,442](#) [Method and system for waveguide delay based equalization with current and optical summing in optical communication](#)
- 34 [10,805,010](#) [Method and system for optoelectronic receivers utilizing waveguide heterojunction phototransistors integrated in a CMOS SOI wafer](#)
- 35 [10,784,964](#) [Method and system for a distributed optical transmitter with local domain splitting](#)
- 36 [10,782,543](#) [Method and system for a distributed mach-zehnder interferometer with integrated feed forward equalizer](#)
- 37 [10,782,479](#) [Method and system for mode converters for grating couplers](#)
- 38 [10,763,807](#) [Method and system for a feedback transimpedance amplifier with sub-40KHZ low-frequency cutoff](#)
- 39 [10,749,542](#) [Method and system for an asynchronous successive approximation register analog-to-digital converter with word completion algorithm](#)
- 40 [10,735,100](#) [Method and system for optical alignment to a silicon photonically-enabled integrated circuit](#)
- 41 [10,734,090](#) [Method and system for a track and hold amplifier with extended dynamic range](#)
- 42 [10,727,944](#) [Method and system for optoelectronics transceivers integrated on a CMOS chip](#)
- 43 [10,721,035](#) [Method and system for an optoelectronic built-in self-test system for silicon photonics optical transceivers](#)
- 44 [10,712,513](#) [Method and system for two-dimensional mode-matching grating couplers](#)
- 

